

MANAGEMENT

ENOVIA Semiconductor Accelerator for IP Management



ENOVIA® Semiconductor Accelerator™ for IP Management creates a competitive advantage by improving a company's intellectual property (IP) and design reuse process for faster product development and delivery. The product makes it easy for IP catalog owners to classify and organize data in a hierarchy that is tailored to their company's business environment and products.

Key Benefits

- Work with library classification systems that match their role's unique needs
- Classify existing product content from other ENOVIA products or create new product documents with robust document management capabilities
- Search libraries based on all parameters associated with its classes with automatic unit of measure conversions
- Request additional "download" access from a librarian after reviewing library content for items of interest
- Participate in peer reviews before content is made available to the rest of the organization
- Subscribe to library updates and additions to stay informed of critical product content changes
- Use semiconductor industry standard taxonomy and attributes as defined by VSIA

Product Overview

Time-to-market in the Semiconductor market has become the critical factor in generating profit early in a product's lifecycle. Product complexity and the breadth of products have outpaced the productivity gains needed for companies to maintain a competitive advantage. Tools that increase the rate of development for product content are essential for improving design and production efficiencies.

Reuse of product content, per current studies, have shown a 70% savings in product design time. The infrastructure for cataloging, finding, and managing reusable data is a key element to realizing these savings. Since an inefficient reuse system can diminish the actual savings, a system that fits the company's business and data needs will inevitably enjoy better adoption rates and is essential for translating this potential savings into bottom line profits.

With ENOVIA Semiconductor Accelerator for IP Management, users can access product content libraries tailored for their specific needs. ENOVIA Semiconductor Accelerator for IP Management provides simple and efficient search and navigation capabilities. Whether using the hierarchical tree-browser or the powerful parametric search capabilities, it is quick and easy for users to search library repositories thoroughly to find the best available match for their criteria. This in turn maximizes the reuse achieved and the savings returned on the investment in the company's IP.

ENOVIA Semiconductor Accelerator for IP Management also provides an interface to semiconductor data managed by the ENOVIA® Synchronicity® DesignSync® Central™ or the ENOVIA® Synchronicity® DesignSync® Data Manager. Design data can be moved directly to the IP reuse catalog or can be linked directly into the catalog by references, allowing the original to remain in the work-in-process server.

Product Highlights

Key features and capabilities for ENOVIA Semiconductor Accelerator for IP Management include:

Parametric Searches and Comparisons

The powerful, yet simple to use, search capability makes it easy to locate product content based on complex sets of reuse criteria. ENOVIA Semiconductor Accelerator for IP Management provides advanced search options for searching the entire library and classification attribute definitions. Searching across different units of measure is simplified as the software automatically compares the specified search criteria against the database regardless of how the parameters were captured originally. An advanced comparison tool further analyzes search results to identify differences for ranking alternatives. The simplicity of the search tools encourages end users to look for the best available content always, instead of settling for the first adequate content encountered.

Role-Based Library Classifications

Users work with libraries that have been defined with classification taxonomies unique to their role. The same content may exist in more than one library, but users can browse and search within the role-specific taxonomy of just one library. This makes it very easy for each role to find data from within the context of their own domain.

Library Security

A flexible access control mechanism allows librarians to determine which users and/or roles have access to the library classification structures. Only specified users can view and navigate the libraries, the library structures, and the content. Limited access can be granted to allow users to navigate the data, but not download classified content or IP. However, users can then request download access from the librarian for the given IP, which may be granted on a temporary basis with a time limitation for downloads.

Document Management

ENOVIA Semiconductor Accelerator for IP Management allows end users to classify product content from any ENOVIA solution or it can be configured to create new product related documents of any type. Each document type can be assigned unique release processes and access controls. As a document proceeds through its lifecycle, different user roles can have access to download its files or upload a new version. When making changes, the document can be locked to visually cue other users that a change is being made.

Electronic Review Processes

The system can be optionally set up to enforce cross-functional reviews of library content before releasing it to a broader group of end users. Routes are used to circulate the content to select individuals for review and approval. Route creation and setup can be simplified further by the use of route templates, which pre-define the steps that must be followed. The review process complies with the U.S. Food and Drug Administration's (FDA) 21 Code of Federal Regulations (CFR) Part 11 for recording electronic signatures. All users can benefit from capabilities such as password verification, password aging, password format checks, failed attempts logging, and requiring two administrators to alter a user's password.

Notification of Changes

Users can subscribe to automatic notifications for key events such as new content being added to the library or changes made to classified items. Subscriptions can be attached to entire libraries or to individual components. Notification comes in the form of automatically generated email and can include attached descriptions and links to the relevant library or components for easy navigation. This means that users making use of data from the library can make decisions based on up-to-the-minute status of issues and updates, rather than static knowledge captured at the time of reuse.

Semiconductor IP taxonomy

Industry standard taxonomy and attributes based VSI Alliance (VSIA) standards enable companies to customize classification definition quickly to meet a company's specific products and IP.

Semiconductor Design Data File Upload and Download

Deliverables are linked directly to semiconductor data managed by ENOVIA® Synchronicity® DesignSync® Central® or ENOVIA® Synchronicity® DesignSync® Data Manager. Users in the ENOVIA environment can browse the semiconductor design data directory hierarchy, view the content, and checkout/ in as required. A centralized enterprise-wide system can work with multiple project-level DesignSync servers across the organization or multiple project-level systems may be set up.

The role of ENOVIA V6 and PLM 2.0

ENOVIA Semiconductor Accelerator for IP Management supports PLM 2.0, product lifecycle management online for everyone, and the ENOVIA V6 values: global collaboration innovation, single PLM platform for intellectual property (IP) management, online creation and collaboration, ready to use PLM business processes, and lower cost of ownership.



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